

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

# U.S. UTILITY Patent Application

198	O.I.P.E.	PATENT DATE
SCANNED	Q.A.	

APPLICATION NO. 09/574653	CONT/PRIOR	CLASS <del>25</del> 437	SUBCLASS 199	ART UNIT <del>28</del> 2323	EXAMINER H. Lee
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Youngmin Kim  
Shawn Walsh

## APPLICANTS

# A tunable sidewall spacer process for cmos integrated circuits

## TITLE:

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg	Figs. Drwg	Print Fig	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
	_____ (Legal Instruments Examiner) (Date)			Amount Due	Date Paid
	_____ (Date)			<b>ISSUE BATCH NUMBER</b>	

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